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SERIAL NUMBER 10/602,916	FILING DATE 06/25/2003  RULE	CLASS 712	GROUP ART UNIT 2181	ATTORNEY DOCKET NO. 10971390-3
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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a CON of 09/496,845 02/02/2000 PAT 6,618,801

TM 01/06/06

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 NONE TM 01/06/06

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 09/09/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged Examiner's Signature <i>Monahan</i> Initials <i>TM 01/06/06</i>	STATE OR COUNTRY CO	SHEETS DRAWING 2	TOTAL CLAIMS 47 16	INDEPENDENT CLAIMS 4 6
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 FORT COLLINS , CO  
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TITLE  
 Method and apparatus for implementing two architectures in a chip

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees ( Filing )

TM  
 01/04/2007

FILING FEE  RECEIVED 834	FEES: Authority has been given in Paper	<input type="checkbox"/> 1.17 Fees ( Processing Ext. of
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